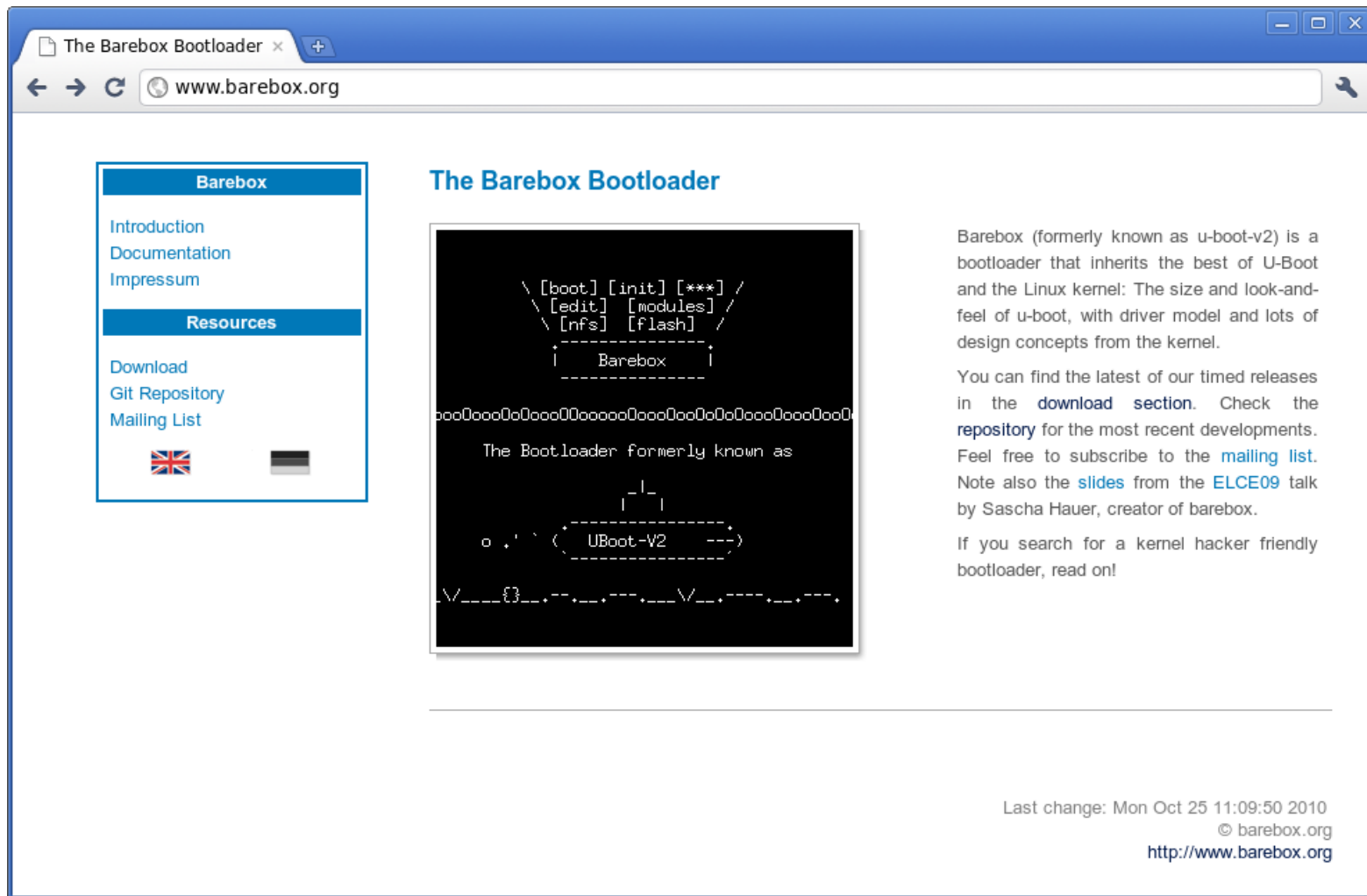


# Booting Linux Fast & Fancy

A screenshot of a web browser displaying the Barebox website. The browser's address bar shows 'www.barebox.org'. The website has a blue header with the title 'The Barebox Bootloader'. On the left, there is a sidebar with a 'Barebox' section containing links for 'Introduction', 'Documentation', and 'Impressum'. Below this is a 'Resources' section with links for 'Download', 'Git Repository', and 'Mailing List', accompanied by the UK and German flags. The main content area features the title 'The Barebox Bootloader' above a terminal window. The terminal shows a boot menu with options like '[boot]', '[init]', '[\*\*\*]', '[edit]', '[modules]', '[nfs]', and '[flash]', with 'Barebox' highlighted. Below the terminal, it says 'The Bootloader formerly known as' followed by a diagram showing 'UBoot-V2' in a dashed box. To the right of the terminal, there is a paragraph of text describing Barebox as a bootloader that inherits the best of U-Boot and the Linux kernel. Below this, it mentions where to find the latest releases and encourages subscribing to the mailing list. At the bottom right, it says 'If you search for a kernel hacker friendly bootloader, read on!'. The footer of the website includes the text 'Last change: Mon Oct 25 11:09:50 2010', '© barebox.org', and 'http://www.barebox.org'.

Embedded Linux Conference Europe  
Cambridge, 2010-10-28  
Robert Schwebel <r.schwebel@pengutronix.de>



# Motivation: Booting Linux Fast & Fancy I

- User experience becomes more important:



# Motivation: Booting Linux Fast & Fancy II

- Industrial devices don't look like computers



- And the shouldn't boot slowly like computers...





# Motivation: Booting Linux Fast & Fancy III

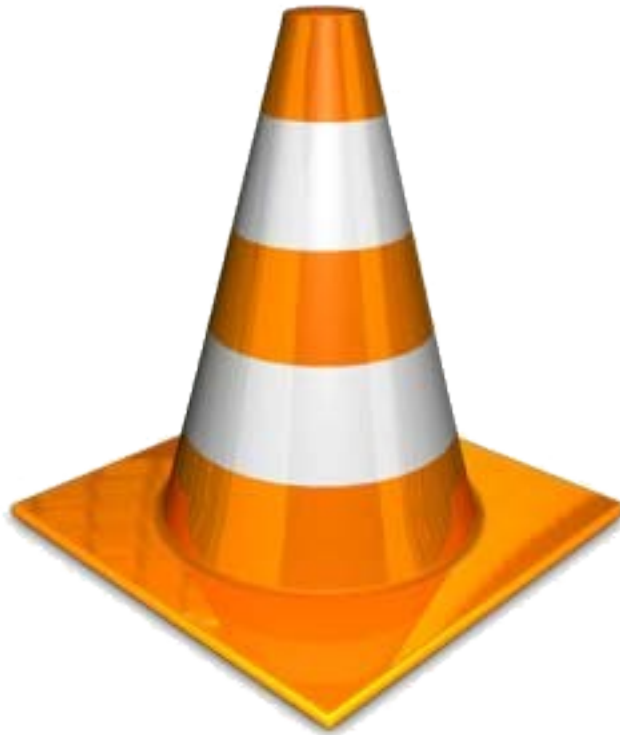
- Automotive devices have fast-boot requirements
- Example from a project:

Answering CAN messages in  
< 200 ms after power-on!



# Motivation: Booting Linux Fast & Fancy IV

- Video: Start sequence on Vortex DX (800 MHz x86)



# What can we do to avoid this?



# Barebox: Project History

- **2007 / u-boot-v2-rc1:**  
Forked from U-Boot,  
as a technology study  
under the “U-Boot-v2” name
- **2009 / barebox-2009.12.0:**  
Renamed to barebox, with  
it's own infrastructure
- **2010 / barebox-2010.10.0**  
20 releases up to now
- Timed releases:  
about once per month
- Maintenance releases:  
on demand





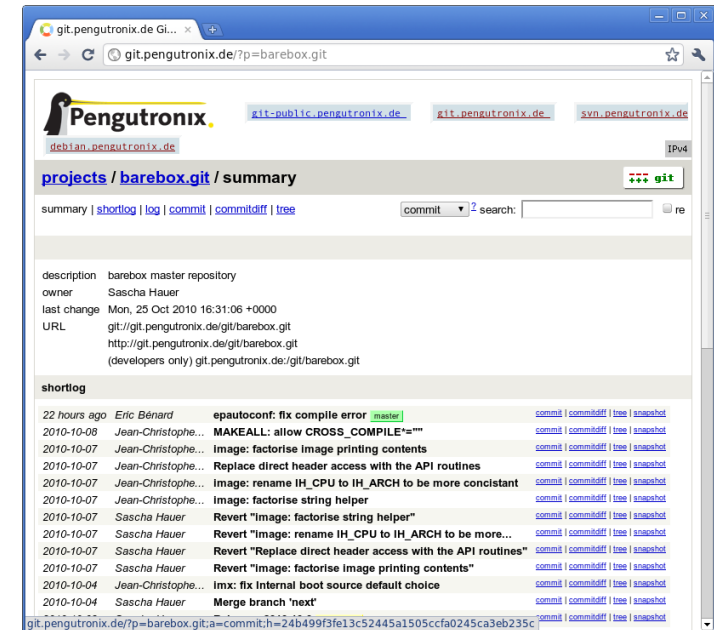
# Barebox: Development Resources

- **Website:**  
<http://www.barebox.org>
- **GIT Server:**  
<http://git.pengutronix.de/?p=barebox.git>

**next** branch:  
accumulates new features

**master** branch:  
next is merged into master after release

- **Mailing List:**  
<http://lists.infradead.org/mailman/listinfo/barebox/>

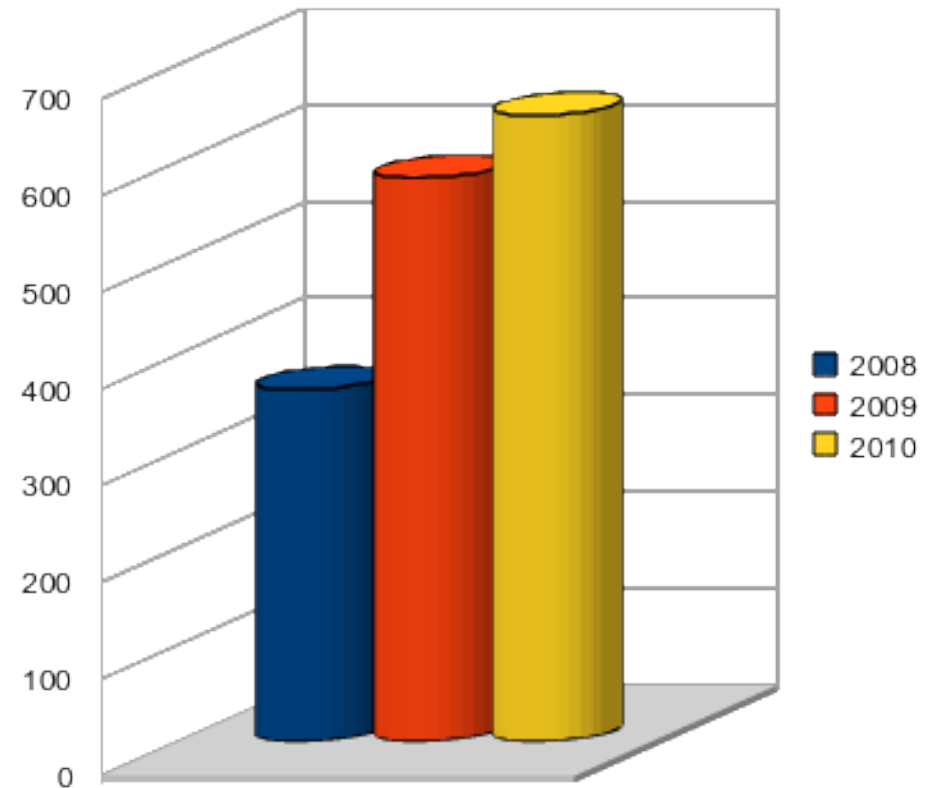




# Barebox: Development Speed

- Commit History:

2008	364
2009	583
2010	648 (until now)



# Barebox: CPU Architectures

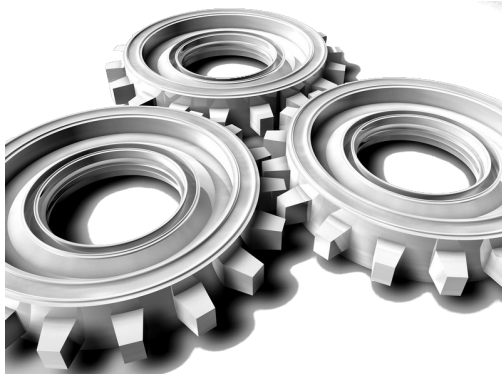
- Supported Hardware:

arm	at91, ep93, i.MX, netX, nomadik, omap, s3c24xx, stm
blackfin	
m68k	mcfv4e
ppc	mpc5xxx
sandbox	linux
x86	bios based

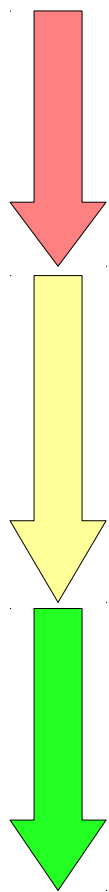


# Barebox - All Features on One Slide

- Build system: Kconfig, Kbuild
- Boot media: linux16, nand, ubi, sd
- Data Transport: DFU, Kermit, X,/Y/Z-Modem, tftp
- Graphics: Framebuffer, splash screen
- Filesystem: cd, ls, cp, saveenv/loadenv, mount, partitions
- Tools: crc, edit, gpio, unlzo
- User interaction: login, menu
- Drivers: i2c, mfd, flash, serial, spi, usb host+device
- Modules: insmod, lsmod
- Memory: meminfo, memtest, md, mw
- Network: ipv4, dhcp, netconsole, tftp, rarp, ping, nfs, dns



# Booting Linux Fast



Power-controller releases reset line

ROM bootloader starts running

Fetch boot block from NAND / SD card

Execute first boot code

Initialize hardware

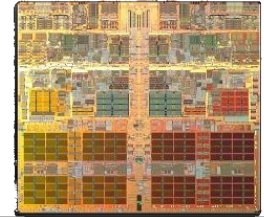
Fetch Linux kernel from NAND / SD card

Execute Linux

Extract compressed image

Kernel boots, initializes hardware

/sbin/init

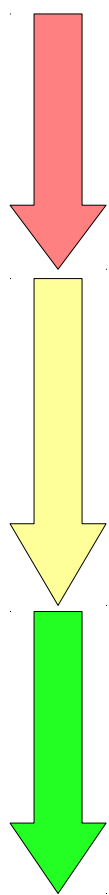


**Optimize Hardware  
(Electronics)**





# Booting Linux Fast



Power-controller releases reset line

ROM bootloader starts running

Fetch boot block from NAND / SD card

Execute first boot code

Initialize hardware

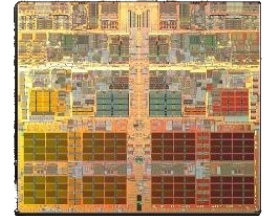
Fetch Linux kernel from NAND / SD card

Execute Linux

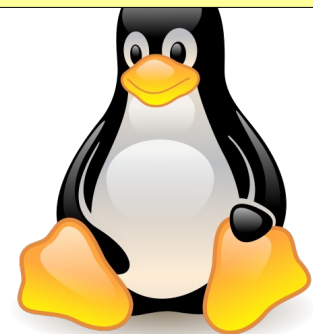
Extract compressed image

Kernel boots, initializes hardware

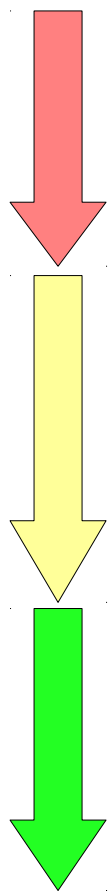
/sbin/init



Select CPUs optimized  
for fastboot (i.e. MX25)



# Booting Linux Fast



Power-controller releases reset line

ROM bootloader starts running

Fetch boot block from NAND / SD card

Execute first boot code

Initialize hardware

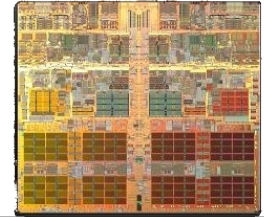
Fetch Linux kernel from NAND / SD card

Execute Linux

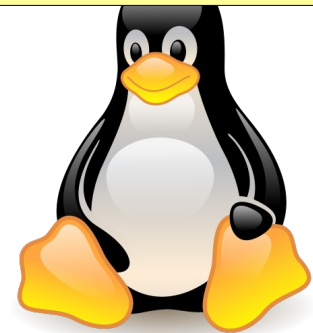
Extract compressed image

Kernel boots, initializes hardware

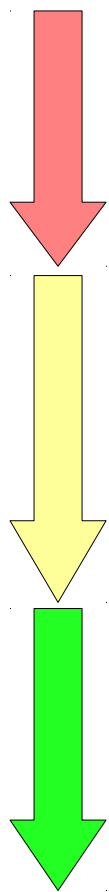
/sbin/init



Done by firmware,  
cannot be tuned,  
usually.



# Booting Linux Fast



Power-controller releases reset line

ROM bootloader starts running

Fetch boot block from NAND / SD card

Execute first boot code

Initialize hardware

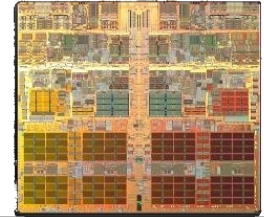
Fetch Linux kernel from NAND / SD card

Execute Linux

Extract compressed image

Kernel boots, initializes hardware

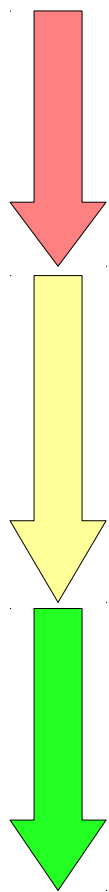
/sbin/init



First place we can  
do something  
in software



# Booting Linux Fast



Power-controller releases reset line

ROM bootloader starts running

Fetch boot block from NAND / SD card

Execute first boot code

Initialize hardware

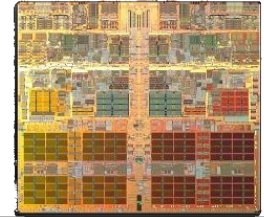
Fetch Linux kernel from NAND / SD card

Execute Linux

Extract compressed image

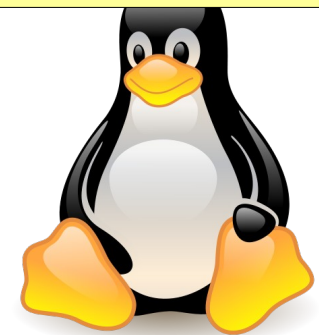
Kernel boots, initializes hardware

/sbin/init



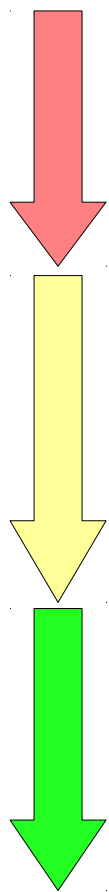
Do only what's  
absolutely necessary.

Tune clocks &  
timings.





# Booting Linux Fast



Power-controller releases reset line

ROM bootloader starts running

Fetch boot block from NAND / SD card

Execute first boot code

Initialize hardware

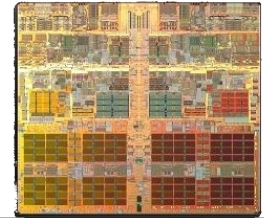
Fetch Linux kernel from NAND / SD card

Execute Linux

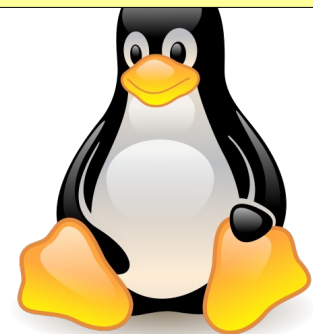
Extract compressed image

Kernel boots, initializes hardware

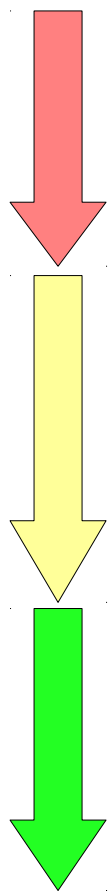
/sbin/init



Async read and  
decompression  
tricks



# Booting Linux Fast



Power-controller releases reset line

ROM bootloader starts running

Fetch boot block from NAND / SD card

Execute first boot code

Initialize hardware

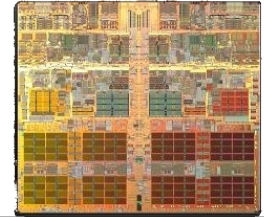
Fetch Linux kernel from NAND / SD card

Execute Linux

Extract compressed image

Kernel boots, initializes hardware

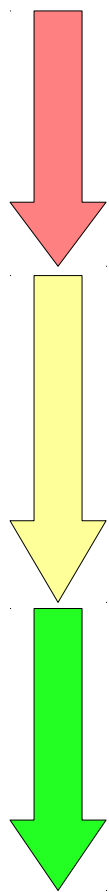
/sbin/init



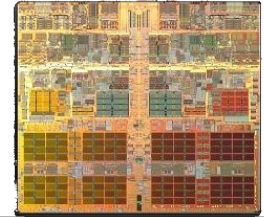
Depending on CPU,  
use uncompressed  
Image or zImage



# Booting Linux Fast



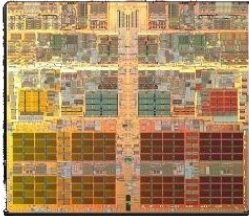
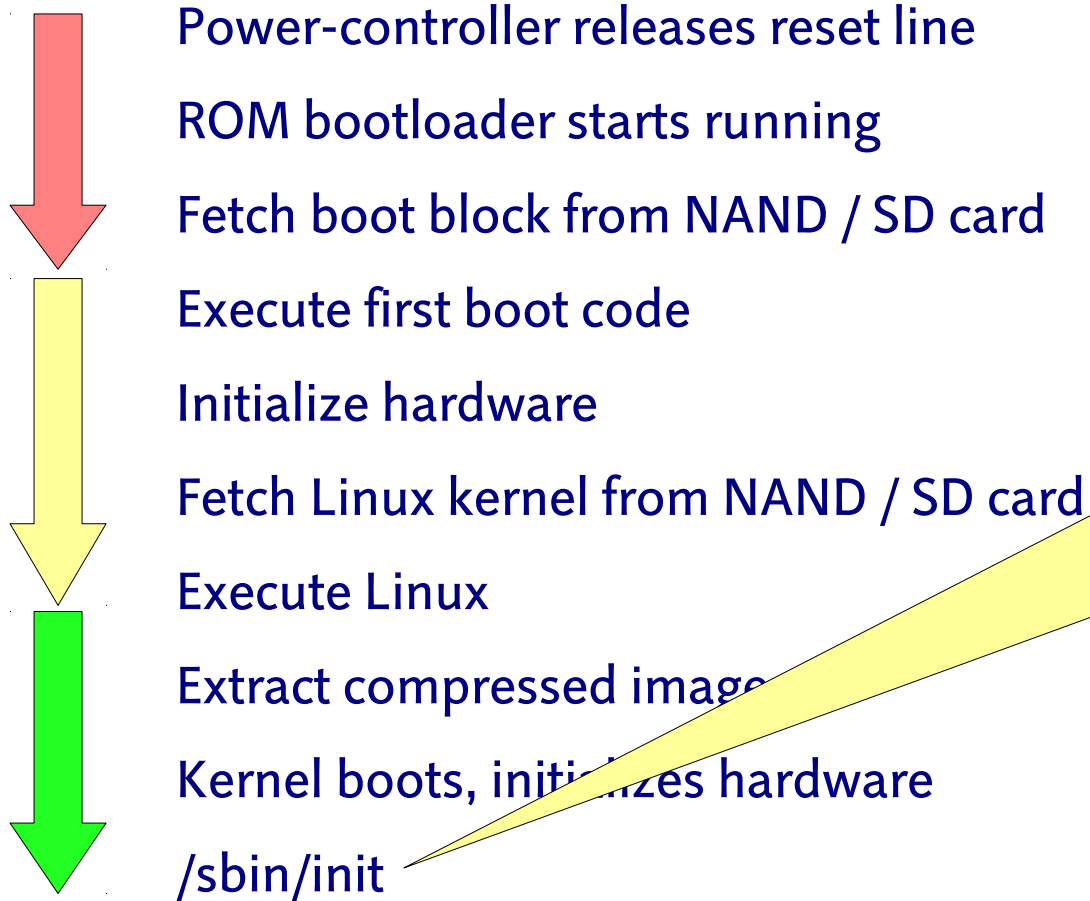
Power-controller releases reset line  
ROM bootloader starts running  
Fetch boot block from NAND / SD card  
Execute first boot code  
Initialize hardware  
Fetch Linux kernel from NAND / SD card  
Execute Linux  
Extract compressed image  
Kernel boots, initializes hardware  
/sbin/init



Minimized kernel,  
all the tricks from  
[elinux.org wiki](http://elinux.org/wiki)



# Booting Linux Fast

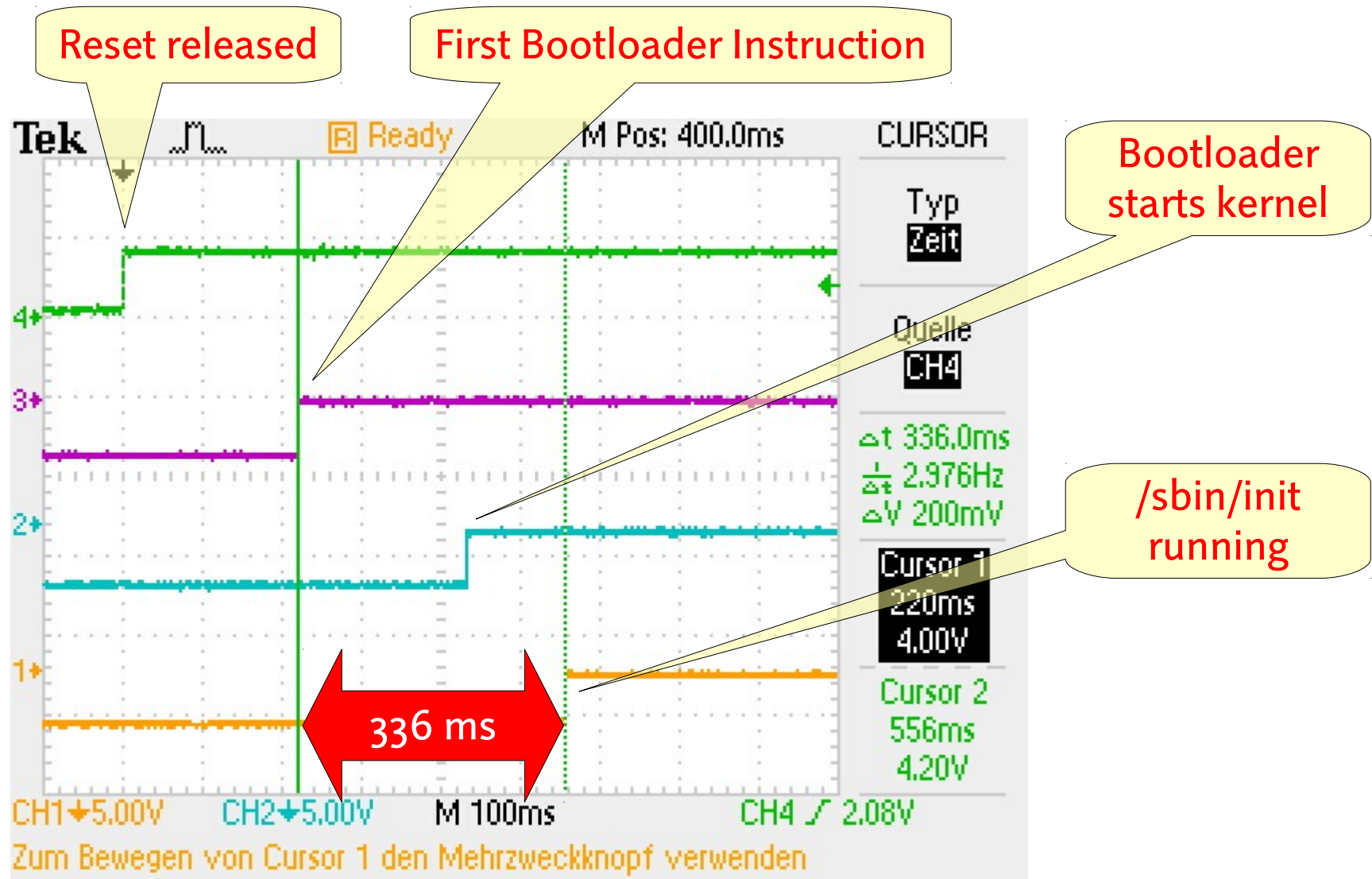


Depending on  
use case,  
use initramfs or  
real roots  
(slower)





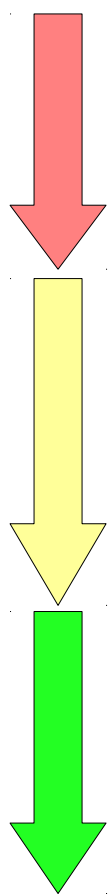
# Booting Linux Fast



Example: Freescale i.MX35, ARM1136EJ-S, 532 MHz



# Booting Linux Fancy



Power-controller releases reset line

ROM bootloader starts running

Fetch boot block from NAND / SD card

Execute first boot code

Initialize hardware

Fetch Linux kernel from NAND / SD card

Execute Linux

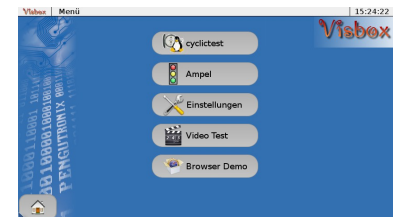
Extract compressed image

Kernel boots, initializes hardware

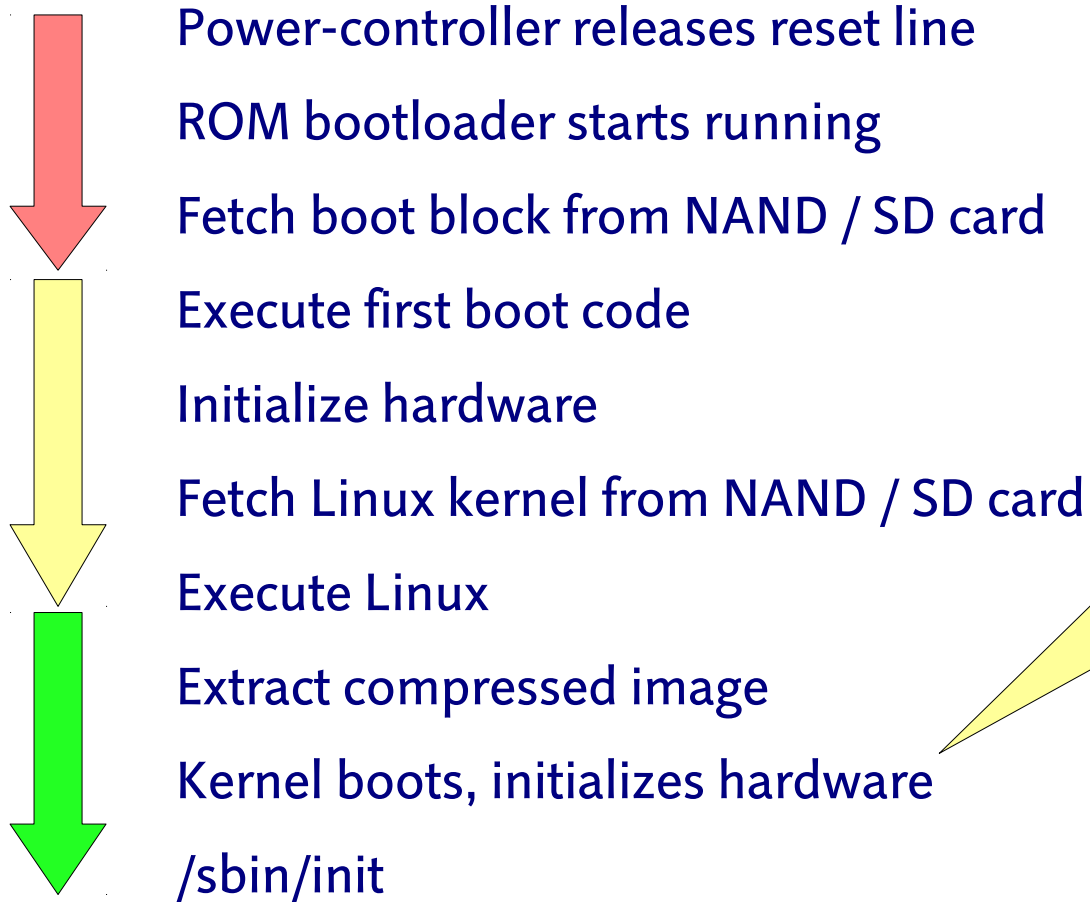
/sbin/init



Backlight off  
Load splash  
Show splash  
Backlight on



# Booting Linux Fancy



Make sure  
Framebuffer  
has fixed address  
between  
bootloader  
and kernel.

No re-init!



# Booting Linux Fancy



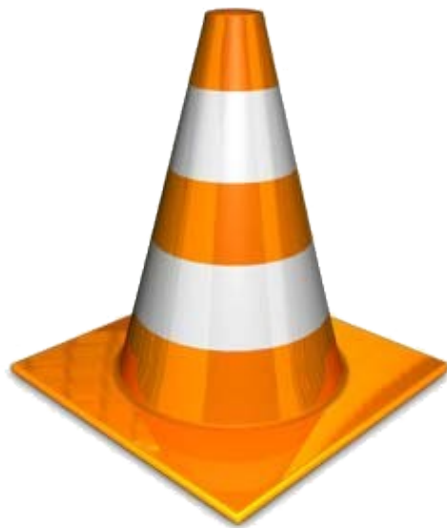
Cross fading  
with  
overlay





# Booting Linux Fancy

- Video: Start sequence on phyCORE-i.MX27  
<http://www.youtube.com/watch?v=F5Cbu1sO4D8>
- Video: Start sequence on NESO (MX27, ARM926, 400 MHz)  
[http://www.youtube.com/watch?v=2FZI\\_7u9nBE](http://www.youtube.com/watch?v=2FZI_7u9nBE)
- Demo: Start sequence on CUPID (MX35, ARM1136, 532 MHz)



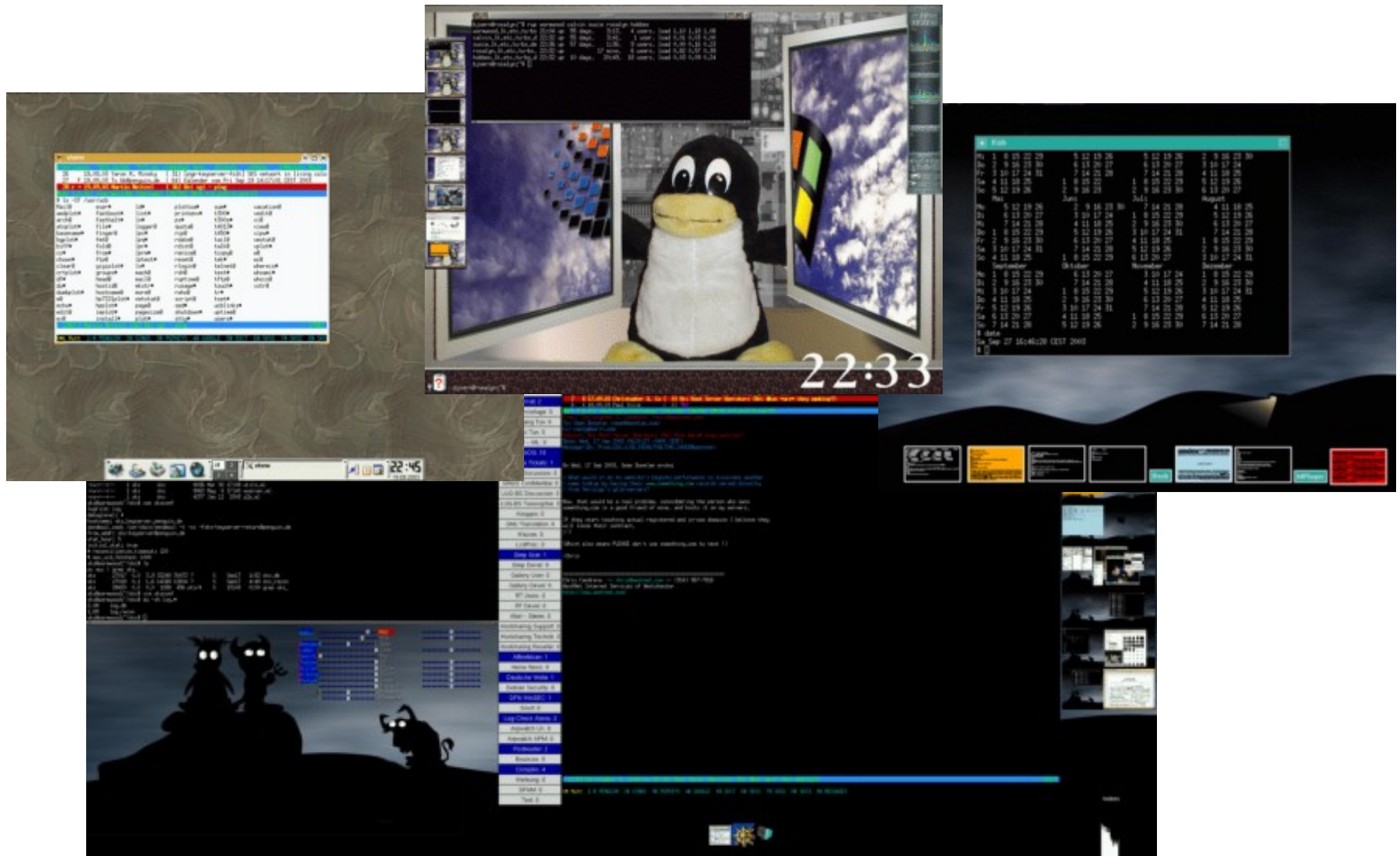
# If fast is not fast enough ...



- We do not reach the 200 ms limit of the automotive guys
- BTCS: **Boot Time Critical Services**  
(originally inspired by Freescale, now implemented with mainline focus)
- Idea:
  - Set aside some memory
  - Register a “poller” in Barebox
  - Make sure memory is handed over to Linux
  - Poller ends up as a normal interrupt service routine in Linux
- We estimate to have CAN ready after about  $< 100$  ms
- No numbers yet, measurement hardware is still under construction...
- Downside: “bare metal” stack



# Thanks for Listening - Questions?





# Do we need a Bootloader at all?

- Alternative: use Linux to boot Linux (less code duplication)  
(See John's talk yesterday)
- Booting from NAND: we need at least a pre-loader dealing with Bad Blocks / UBI
- ROM access routines to NAND and SD are unoptimized
- Barebox offers a (to kernel developers) well-known structure, where to put code in *if* it is necessary.
- Minimal porting effort, no parallel running code
- Even if we have linux-only booting in the future, Barebox can be scaled down to the minimum, for the first stage

